

SEMITRANS™ 3

Trench IGBT Module

SKM 600GB126D

SKM 600GAL126D

Preliminary Data

Features

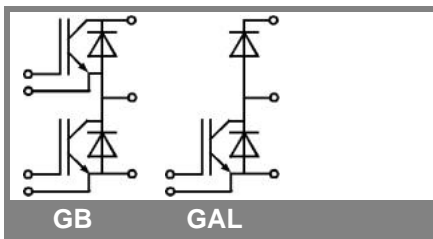
- Homogeneous Si
- Trench = Trenchgate technology
- V_{CEsat} with positive temperature coefficient
- High short circuit capability, self limiting to $6 \times I_C$

Typical Applications

- AC inverter drives
- UPS
- Electronic welders

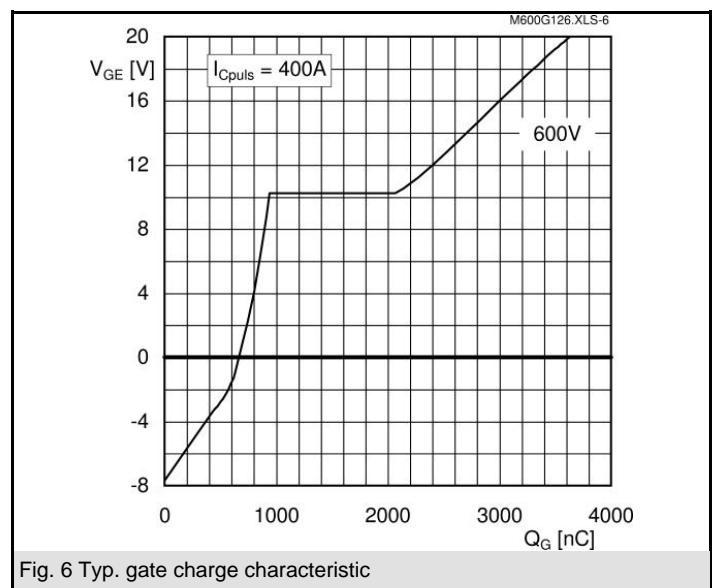
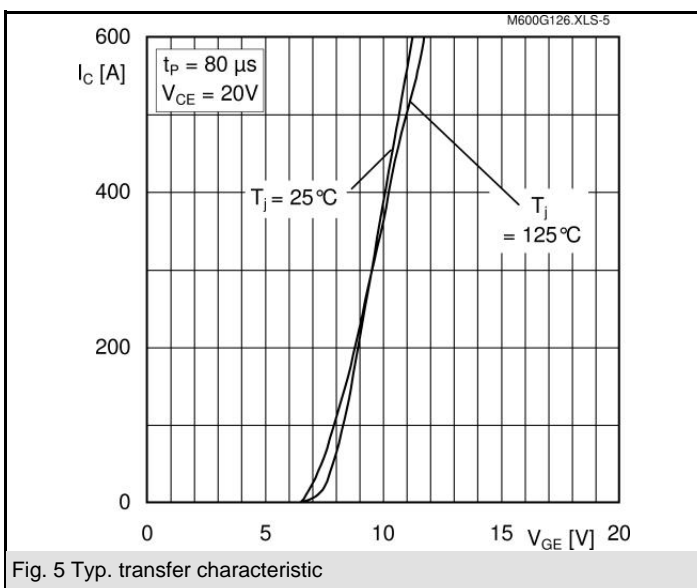
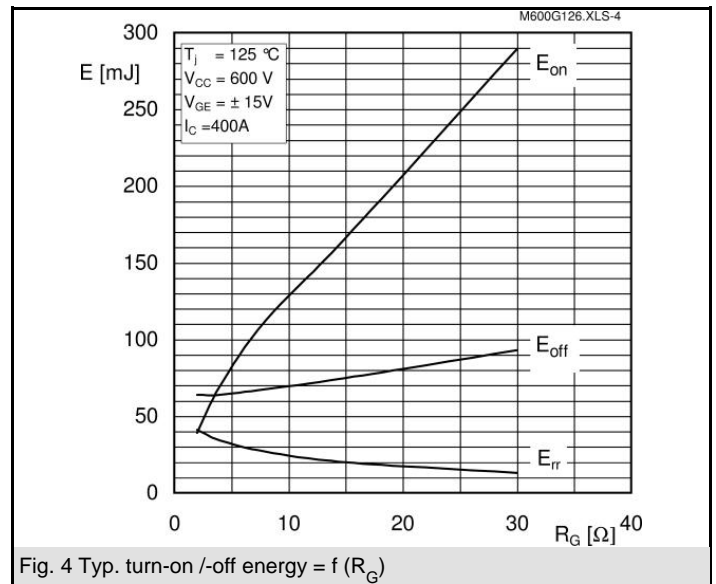
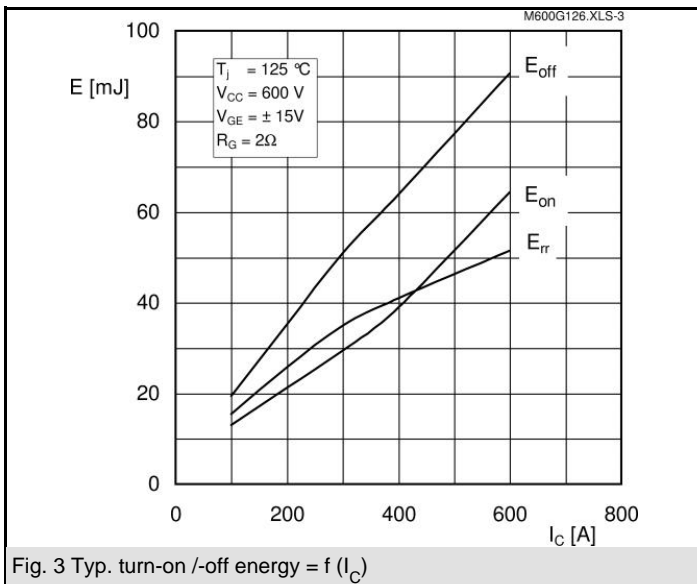
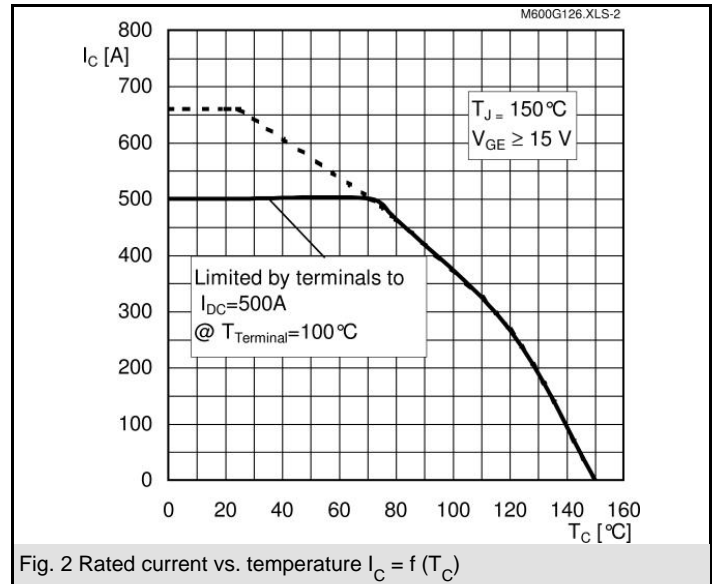
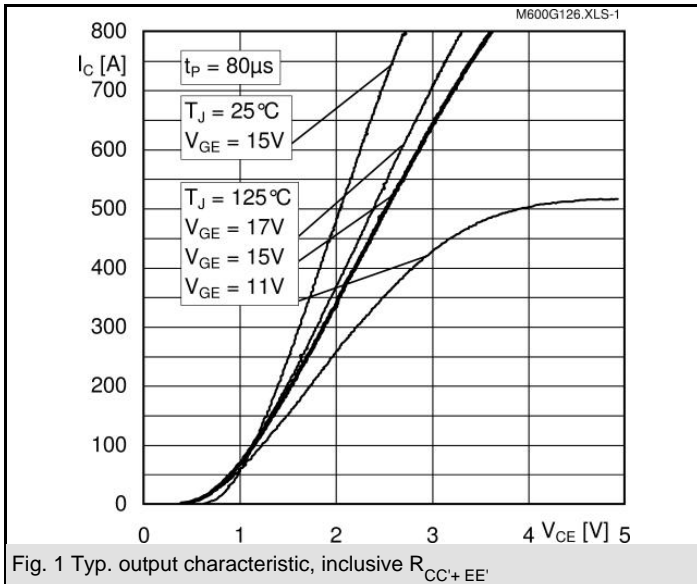
Remarks

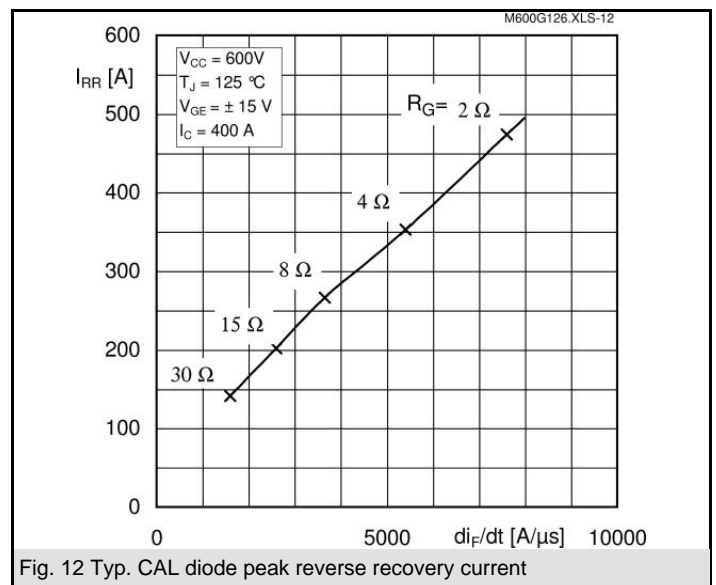
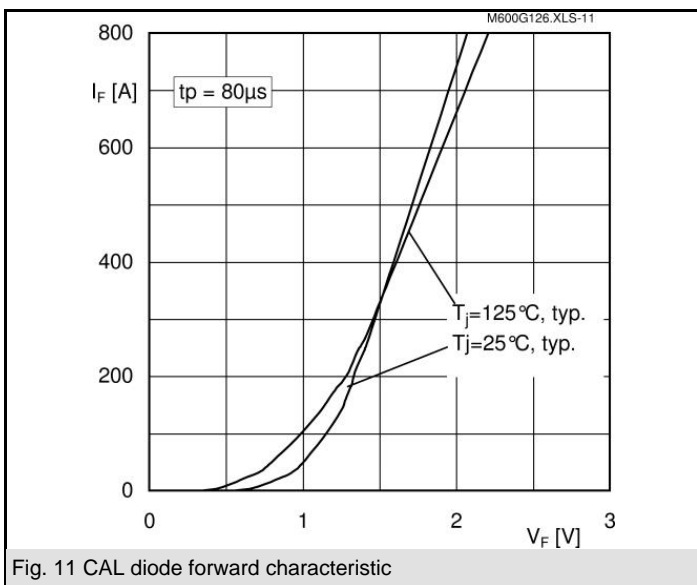
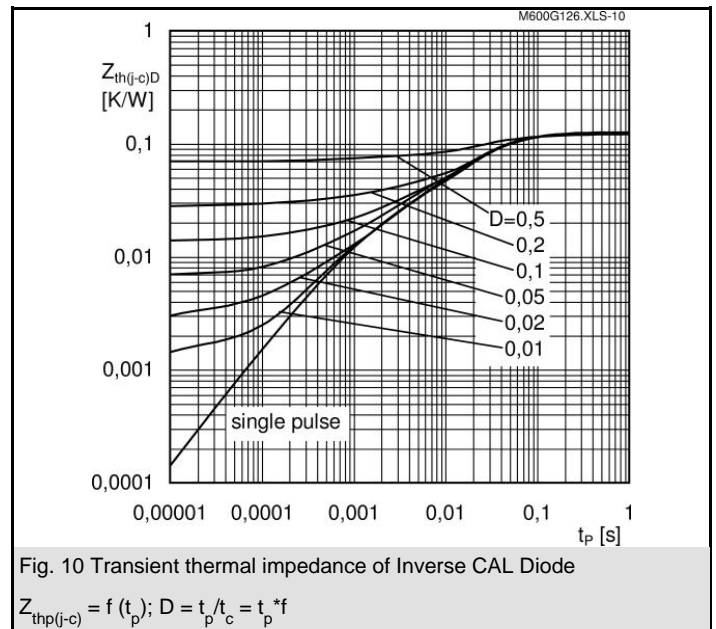
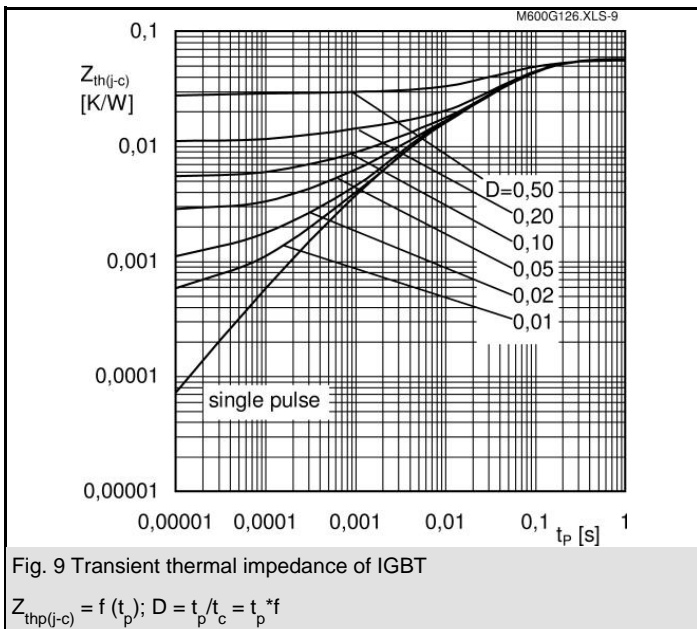
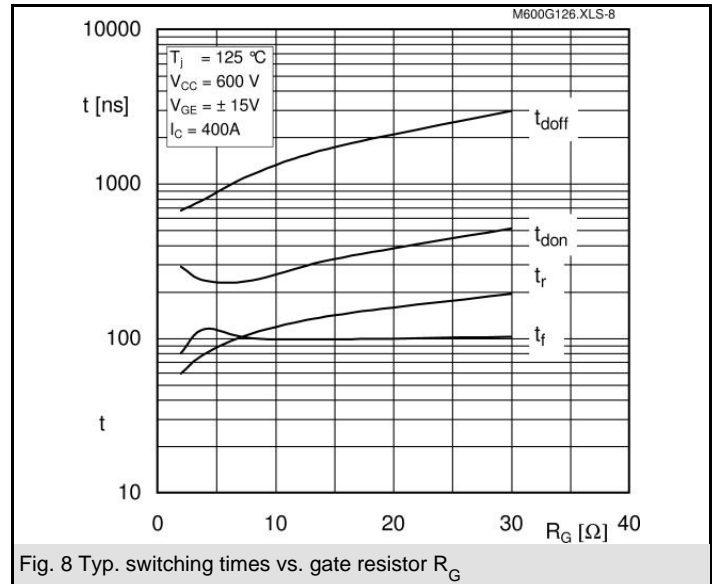
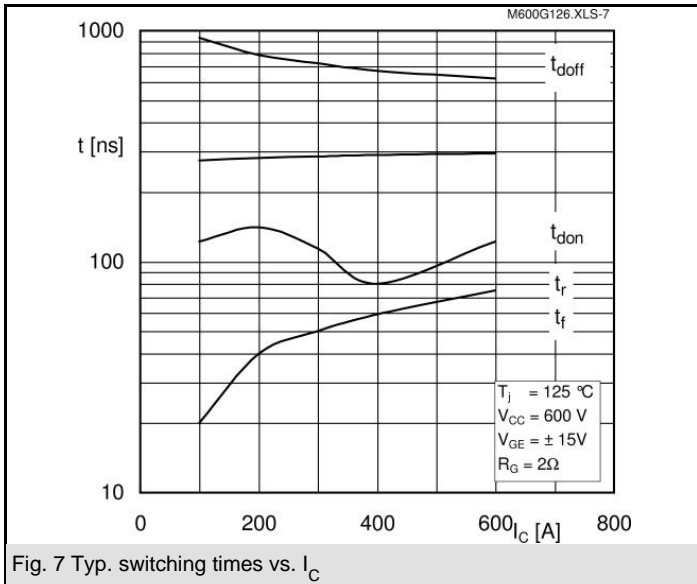
- $I_{DC} \leq 500A$ for $T_{Terminal} = 100\text{ }^\circ\text{C}$

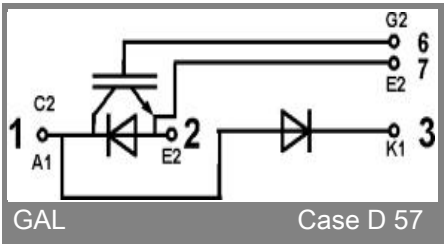
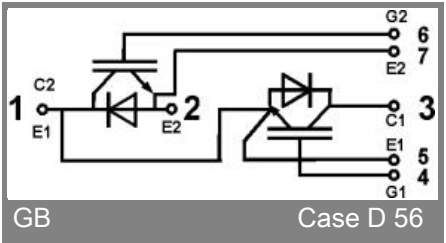
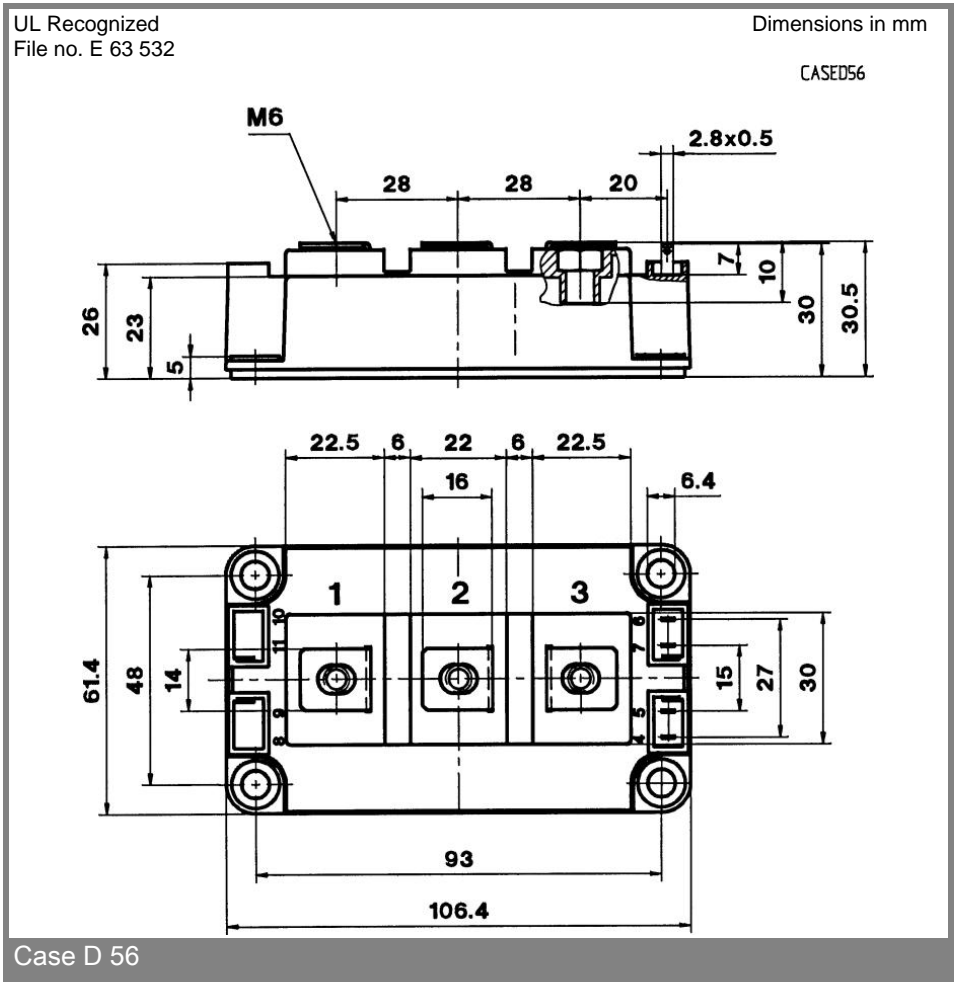
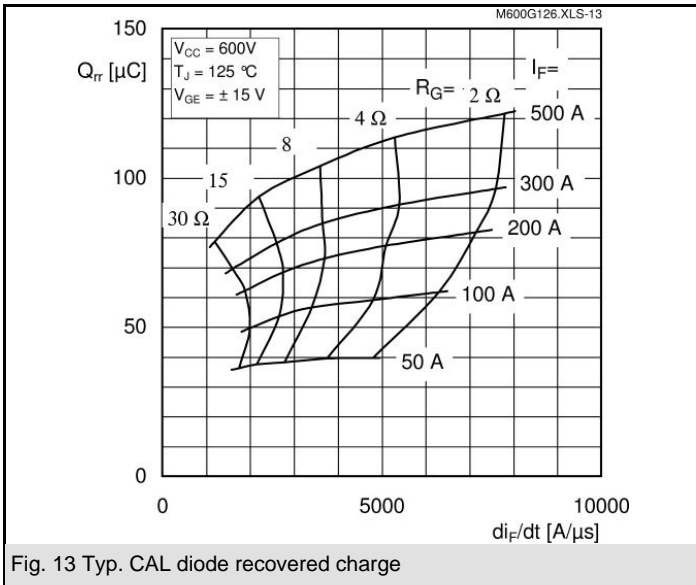


Absolute Maximum Ratings		$T_c = 25\text{ }^\circ\text{C}$, unless otherwise specified	
Symbol	Conditions	Values	Units
IGBT			
V_{CES}		1200	V
I_C	$T_c = 25\text{ (80) }^\circ\text{C}$	660 (460)	A
I_{CRM}	$T_c = 25\text{ (80) }^\circ\text{C}$, $t_p = 1\text{ ms}$	1320 (920)	A
V_{GES}		± 20	V
T_{vj} (T_{stg})	$T_{OPERATION} \leq T_{stg}$	- 40 ... + 150 (125)	$^\circ\text{C}$
V_{isol}	AC, 1 min.	4000	V
Inverse diode			
I_F	$T_c = 25\text{ (80) }^\circ\text{C}$	490 (340)	A
I_{FRM}	$T_c = 25\text{ (80) }^\circ\text{C}$, $t_p = 1\text{ ms}$	1320 (920)	A
I_{FSM}	$t_p = 10\text{ ms}$; sin.; $T_j = 150\text{ }^\circ\text{C}$	2900	A
Freewheeling diode			
I_F	$T_c = 25\text{ (80) }^\circ\text{C}$	490 (340)	A
I_{FRM}	$T_c = 25\text{ (80) }^\circ\text{C}$, $t_p = 1\text{ ms}$	1320 (920)	A
I_{FSM}	$t_p = 10\text{ ms}$; sin.; $T_j = 150\text{ }^\circ\text{C}$	2900	A

Characteristics		$T_c = 25\text{ }^\circ\text{C}$, unless otherwise specified			
Symbol	Conditions	min.	typ.	max.	Units
IGBT					
$V_{GE(th)}$	$V_{GE} = V_{CE}$, $I_C = 16\text{ mA}$	5	5,8	6,5	V
I_{CES}	$V_{GE} = 0$, $V_{CE} = V_{CES}$, $T_j = 25\text{ (125) }^\circ\text{C}$		0,2	0,6	mA
$V_{CE(TO)}$	$T_j = 25\text{ (125) }^\circ\text{C}$		1 (0,9)	1,2 (1,1)	V
r_{CE}	$V_{GE} = 15\text{ V}$, $T_j = 25\text{ (125) }^\circ\text{C}$		1,8 (2,8)	2,4 (3,4)	m Ω
$V_{CE(sat)}$	$I_C = 400\text{ A}$, $V_{GE} = 15\text{ V}$, chip level		1,7 (2)	2,15 (2,45)	V
C_{ies}	under following conditions		32		nF
C_{oes}	$V_{GE} = 0$, $V_{CE} = 25\text{ V}$, $f = 1\text{ MHz}$		11		nF
C_{res}			2,2		nF
L_{CE}				20	nH
$R_{CC'+EE'}$	res., terminal-chip $T_c = 25\text{ (125) }^\circ\text{C}$		0,35 (0,5)		m Ω
$t_{d(on)}$	$V_{CC} = 600\text{ V}$, $I_C = 400\text{ A}$		290		ns
t_r	$R_{Gon} = R_{Goff} = 2\text{ }^\circ\Omega$, $T_j = 125\text{ }^\circ\text{C}$		60		ns
$t_{d(off)}$	$V_{GE} = \pm 15\text{ V}$		670		ns
t_f			80		ns
E_{on} (E_{off})			39 (64)		mJ
Inverse diode					
$V_F = V_{EC}$	$I_F = 400\text{ A}$; $V_{GE} = 0\text{ V}$; $T_j = 25\text{ (125) }^\circ\text{C}$		1,6 (1,6)	1,8 (1,8)	V
$V_{(TO)}$	$T_j = 25\text{ (125) }^\circ\text{C}$		1 (0,8)	1,1 (0,9)	V
r_T	$T_j = 25\text{ (125) }^\circ\text{C}$		1,5 (2)	1,8 (2,3)	m Ω
I_{RRM}	$I_F = 400\text{ A}$; $T_j = 125\text{ () }^\circ\text{C}$		475		A
Q_{rr}	$di/dt = 7600\text{ A}/\mu\text{s}$		96		μC
E_{rr}	$V_{GE} = 0\text{ V}$		41		mJ
FWD					
$V_F = V_{EC}$	$I_F = 400\text{ A}$; $V_{GE} = 0\text{ V}$, $T_j = 25\text{ (125) }^\circ\text{C}$		1,6 (1,6)	1,8 (1,8)	V
$V_{(TO)}$	$T_j = 25\text{ (125) }^\circ\text{C}$		1 (0,8)	1,1 (0,9)	V
r_T	$T_j = 25\text{ (125) }^\circ\text{C}$		1,5 (2)	1,8 (2,3)	m Ω
I_{RRM}	$I_F = 400\text{ A}$; $T_j = 125\text{ () }^\circ\text{C}$		475		A
Q_{rr}	$di/dt = 7600\text{ A}/\mu\text{s}$		96		μC
E_{rr}	$V_{GE} = 0\text{ V}$		41		mJ
Thermal characteristics					
$R_{th(j-c)}$	per IGBT			0,055	K/W
$R_{th(j-c)D}$	per Inverse Diode			0,125	K/W
$R_{th(j-c)FD}$	per FWD			0,125	K/W
$R_{th(c-s)}$	per module			0,038	K/W
Mechanical data					
M_s	to heatsink M6	3		5	Nm
M_t	to terminals M6	2,5		5	Nm
w				325	g







This is an electrostatic discharge sensitive device (ESDS), international standard IEC 60747-1, Chapter IX.

This technical information specifies semiconductor devices but promises no characteristics. No warranty or guarantee expressed or implied is made regarding delivery, performance or suitability.